

Abstract No. Crof0402

XAS Studies on $\text{ZrO}_2/\text{SiO}_2/\text{Si}$ Gate Stacks for CMOS Applications

S. Sayan¹, M. Croft^{1,2}, E. Garfunkel¹, R. Bartynski¹, X. Zhao¹, D. Vanderbilt¹, E. Gusev³, M. Banaszak Holl⁴
(¹Rutgers U., ²NSLS, ³IBM-Watson, ⁴U. Mich.)

Beamline(s): X19A

The International Technology Roadmap for Semiconductors foresees an ~ 1 nm effective gate oxide thickness for 0.1 μm CMOS technology nodes by 2006¹. In this oxide thickness regime, the use of SiO_2 and SiO_xN_y dielectric layers (below 1.2-1.5 nm) will not be possible for many applications due to intolerable tunneling currents. Therefore the gate dielectric and capacitance scaling have emerged as a critical challenge for future device scaling. One solution being extensively explored is to increase gate capacitance by replacing SiO_2 -based dielectrics with higher permittivity dielectrics. For a given capacitance (electrical thickness), a high-K dielectric should show a lower leakage than SiO_2 .

ZrO_2 has received considerable attention in last few years as one of the potential replacements for SiO_2 in future CMOS devices, due to its high dielectric constant (16-25) and moderately large band gap (5.4 –6.0 eV)²⁻⁴ Zhao et al⁵ have recently reported on the dielectric properties of low-pressure crystal phases of ZrO_2 , where they have clearly showed that the dielectric response of the material is strongly phase dependent.

Several different deposition techniques have been utilized for growth of thin films of ZrO_2 including sputtering, chemical vapor deposition, sol-gel based methods, evaporation/re-oxidation and more recently atomic layer chemical vapor deposition. Thin films prepared on silicon by different methods and even from different precursors can result in quite different physical and electronic properties.

In view of the importance of developing an understanding of the crystal and electronic structure of thin ZrO_2 dielectric films on silicon we are performing Zr- $L_{2,3}$ edge XAS on ZrO_2 films deposited by ALCVD (atomic layer chemical vapor deposition) on 10A $\text{SiO}_x\text{N}_y/\text{p-Si}(100)$. These studies are facilitated by extensive XAS ZrO_2 based compound studies by our group in the past. Preliminary Zr- $L_{2,3}$ results on the thin film materials clearly show well defined spectral features associated with the 4d crystalline field (CF) split empty states. The CF splitting appears smaller in the films as compares to the bulk materials. The fine structure above the Zr- L_3 edge indicates a Zr-O distances typical of the bulk material. Detailed comparison of the XAS results are being made to band structure calculations and inverse photoemission measurements.

¹International Technology Roadmap for Semiconductors and Semiconductor Industry Association, "International Technology Roadmap for Semiconductors," (2000).

²G. D. Wilk, R. M. Wallace, and J. M. Anthony, "High-k gate dielectrics: Current status and materials properties considerations," *Appl. Phys. Rev.* **89** (10) (2001).

³M. Balog, M. Schieber, M. Michman *et al.*, "The chemical vapour deposition and characterization of $\text{ZrO}/\text{sub } 2/$ films from organometallic compounds," *Thin Solid Films* **47** (2), 109-120 (1977).

⁴J. Shappir, A. Anis, and I. Pinsky, "Investigation of MOS capacitors with thin ZrO_2 layers and various gate materials for advanced DRAM applications," *IEEE Transactions on Electron Devices* **ED-33** (4), 442-449 (1986).

⁵Xinyuan Zhao and David Vanderbilt, "Phonons and lattice dielectric properties of zirconia," *Physical Review B* **65** (7), 075105/1-10 (2002).